atomera

Investor Presentation

June 2019

Safe Harbor

This presentation contains forward-looking statements concerning Atomera Incorporated (““Atomera,” the “Company,” “we,” “us,” and “our”). The words “believe,” “may,” “will,” “potentially,” “estimate,” “continue,” “anticipate,” “intend,” “could,” “would,” “project,” “plan,” “expect” and similar expressions that convey uncertainty of future events or outcomes are intended to identify forward-looking statements. These forward-looking statements are subject to a number of risks, uncertainties and assumptions, including those described in the “Risk Factors” section of our Annual Report on Form 10-K for the year ended December 31, 2018 filed with the SEC on March 11, 2019 (the “Annual Report”). In light of these risks, uncertainties and assumptions, the forward-looking events and circumstances discussed in this presentation may not occur and actual results could differ materially and adversely from those anticipated or implied in our forward-looking statements. You should not rely upon forward-looking statements as predictions of future events. Although we believe that the expectations reflected in our forward-looking statements are reasonable, we cannot guarantee that the future results, levels of activity, performance or events and circumstances described in the forward-looking statements will be achieved or occur.

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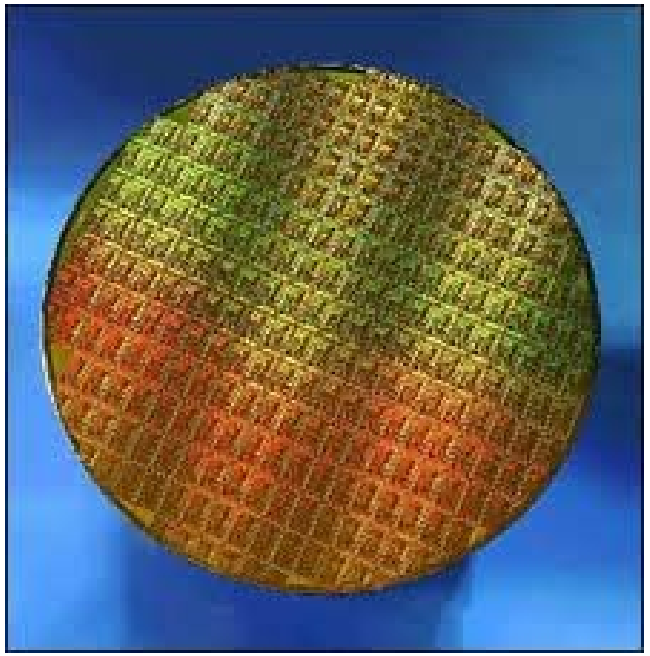
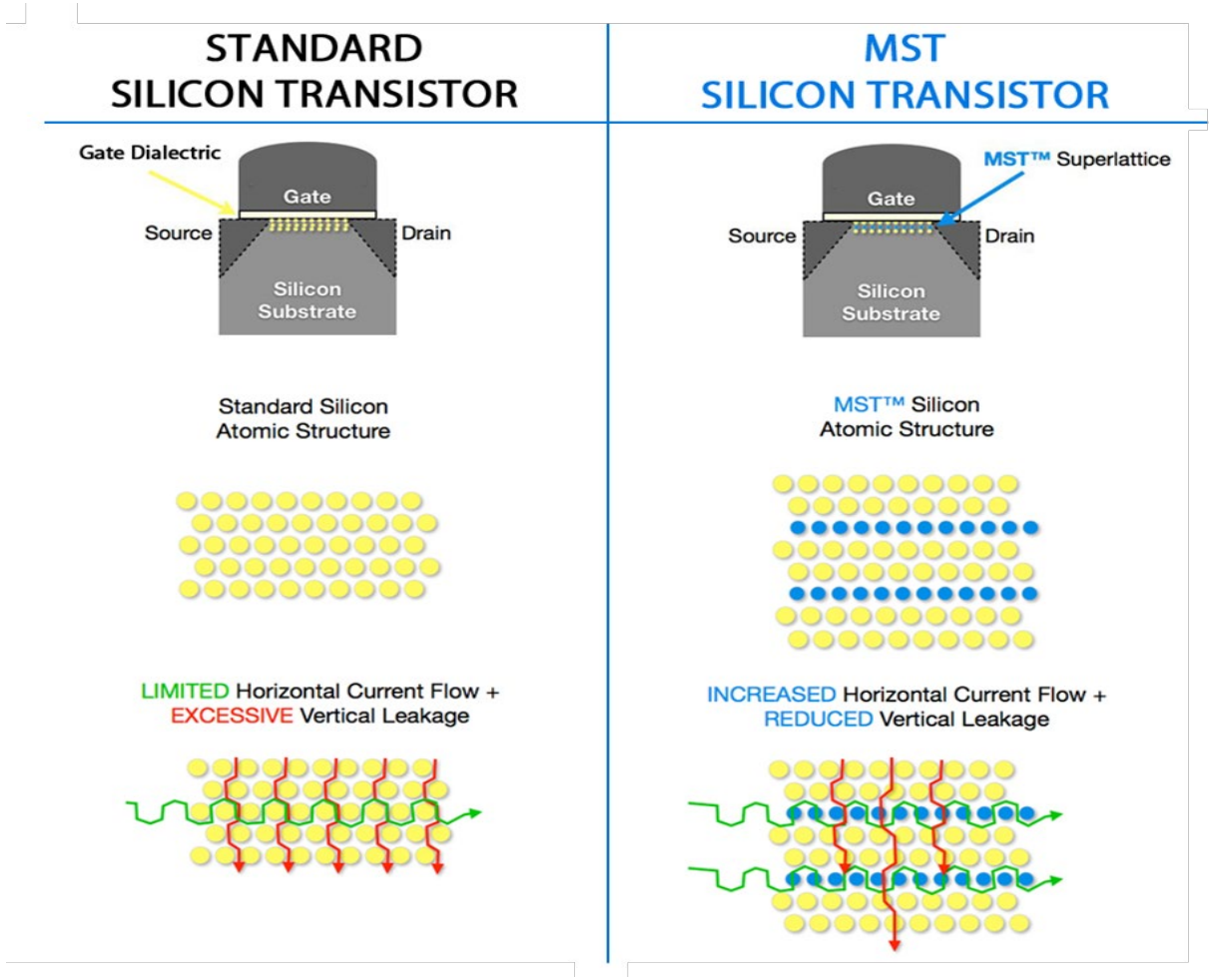
Investment Overview

- **Mears Silicon Technology (MST®) is a thin film used to enhance semiconductors**
  - *Results in higher performance, lower power, and lower costs for ICs*
- **Capital-light IP licensing business**
  - *Robust and growing patent portfolio*
- **Engaged with 50% of world's top semiconductor makers**
- **Licenses with Asahi Kasei Microdevices (AKM) and STMicroelectronics**
- **Strong team to commercialize technology**
  - *CEO ran $1B+ divisions at Broadcom and Altera*
  - *Founder/CTO co-invented the EDFA for long-haul optical applications*
  - *Deeply experienced materials science and semiconductor engineering team*



MST Technology

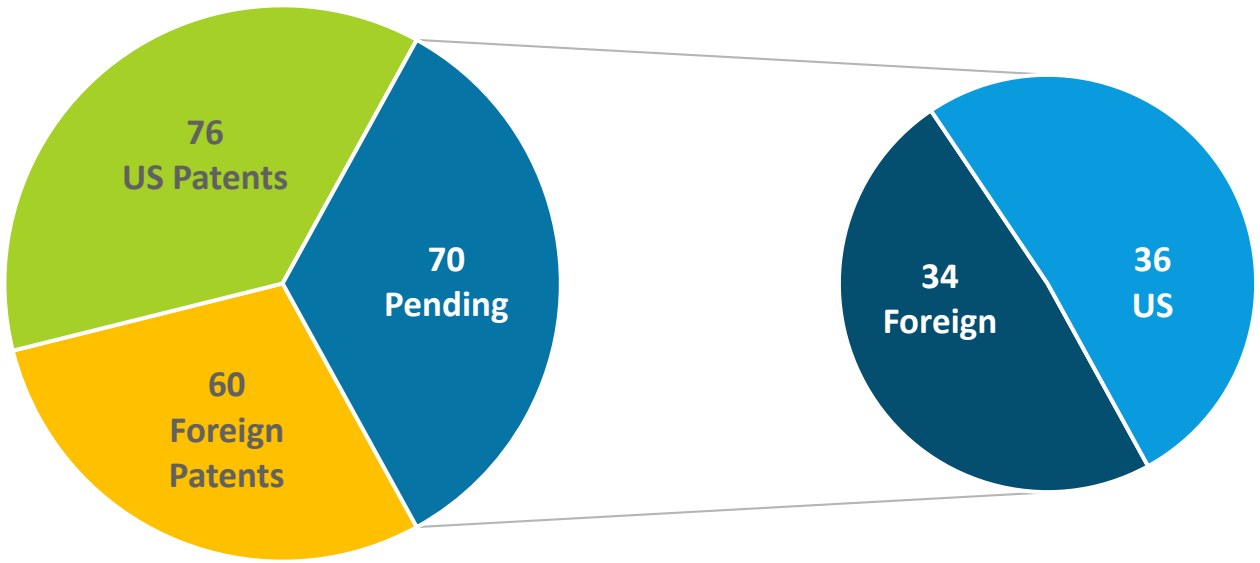


Potential Benefits

- **Improved Efficiency**
  - *Higher transistor performance*
  - *Lower power consumption*
  - *Better reliability*
- **Lower cost**
  - *Reduced die size*
  - *Improved yield*
  - *Higher throughput*
- **Same benefits as a node shrink**

Patent Portfolio

Over 200 Patents Granted and Pending

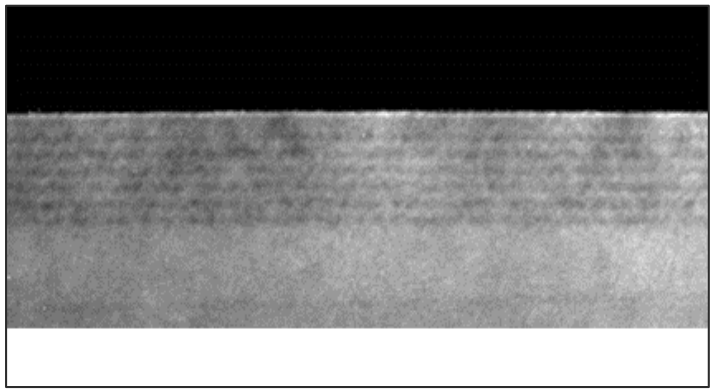


Core MST Method and Device

MST Enabled Devices/Architecture

Next-Gen Architectures using MST

Discoverable



These distinctive layers are visible on products using MST

Extensive know-how

Extends life and value of patents

Semiconductor Ecosystem

atomera

Integrated Device Manufacturers

SAMSUNG
Micron
TOSHIBA
intel
Texas Instruments
ST
Infineon
ON
RENESAS
NXP
AsahiKASEI
SK hynix

Foundry

tsmc
GLOBAL FOUNDRIES
UMC
SMIC
HHGrace
DB HiTek
TOWERJAZZ
MagnaChip

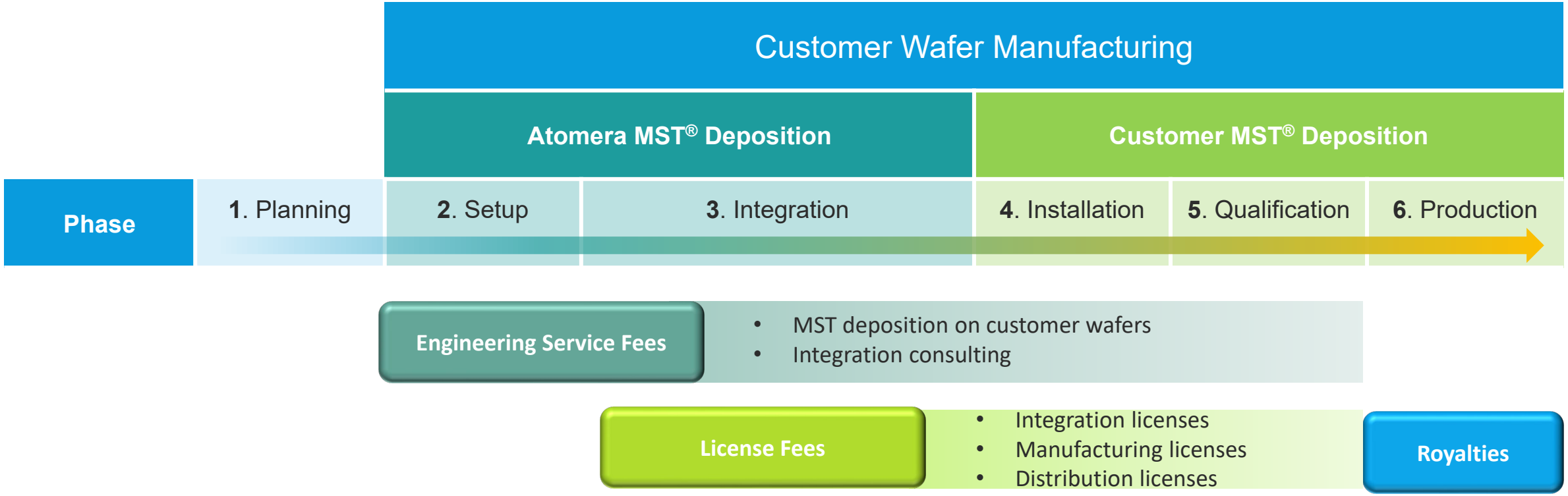
Fabless

BROADCOM
MARVELL
maxim integrated
MICROCHIP
SKYWORKS
QUALCOMM
XILINX
qorvo

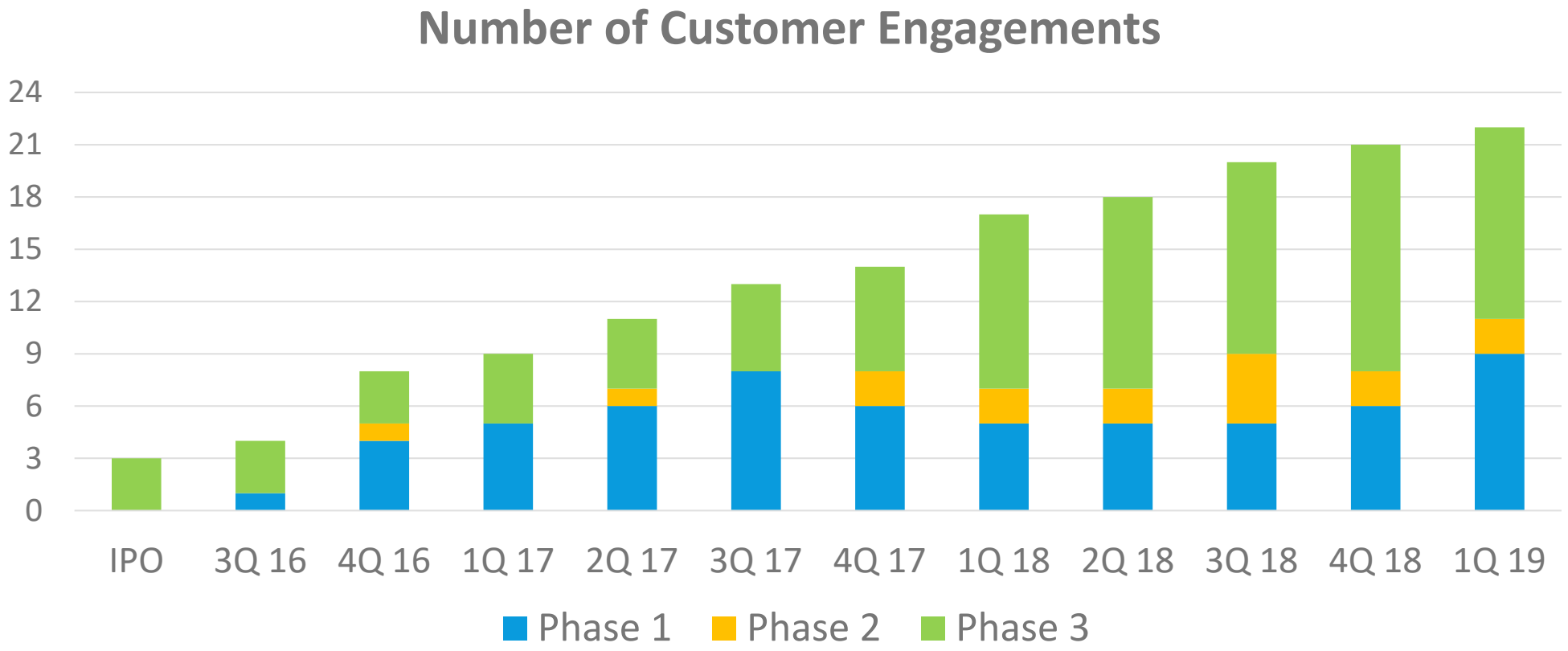
Tool Suppliers

ASM
APPLIED MATERIALS
KOKUSAI SEMICONDUCTOR EQUIPMENT CORPORATION
SYNOPSYS

Customer Engagement Model



Growing Customer Pipeline

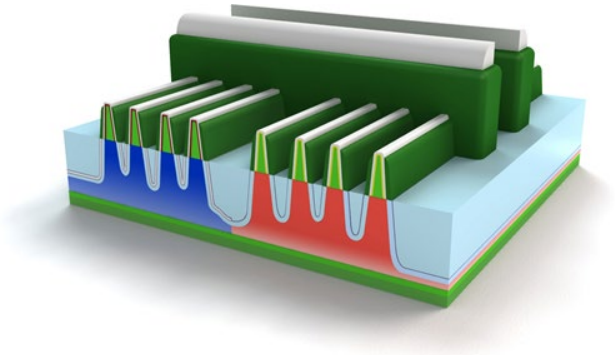


- 22 engagements, 17 customers
- Working with 50% of the world's top semiconductor makers*

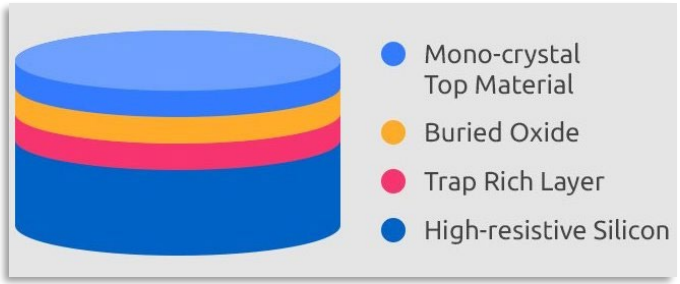
* 10 of the top 20 (IC Insights, McClean Report 2018)

Q1 Breakthroughs

- 1. FinFET and Advanced Nodes
  - Simulations of MST in FinFET devices show very high potential
    - Papers authored by Atomera and UC Berkeley
  - Customer results validated MST performance at advanced process nodes
  - Possibility for MST to be included in industry technology roadmap
  - Market potential: $6.2B in royalties



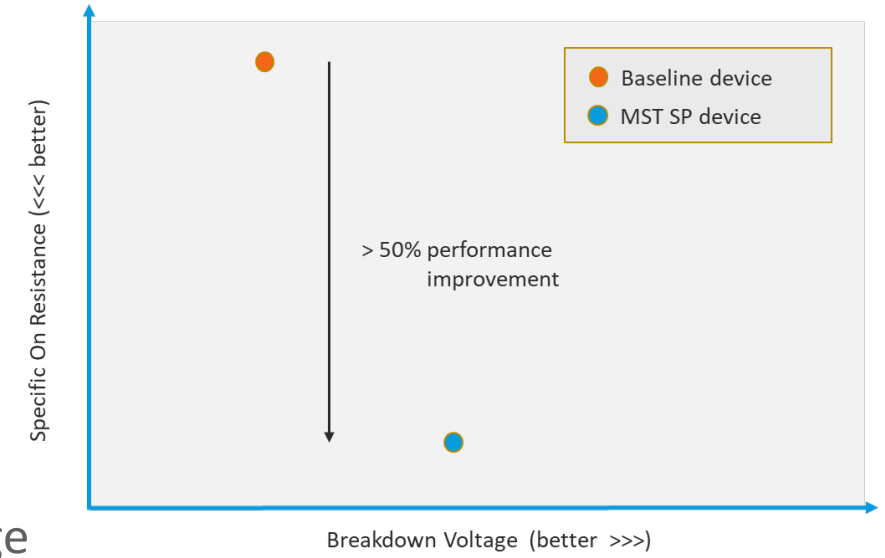
- 2. RF SOI
  - RF SOI is an important enabling technology for 5G cellular
  - Experimental data showed improvement in multiple areas using MST on SOI wafers
    - Paper authored by TI, Atomera, and ATDF
    - Demonstrated 20% mobility, 30% drive current, and leakage improvement
  - Customer results in Q1 validated results from the paper
  - MST now seen as key potential solution for the 5G cellular ramp
  - Market potential: $50M in royalties

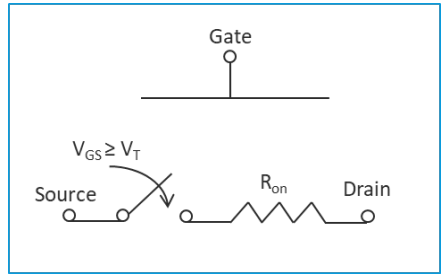


RF SOI

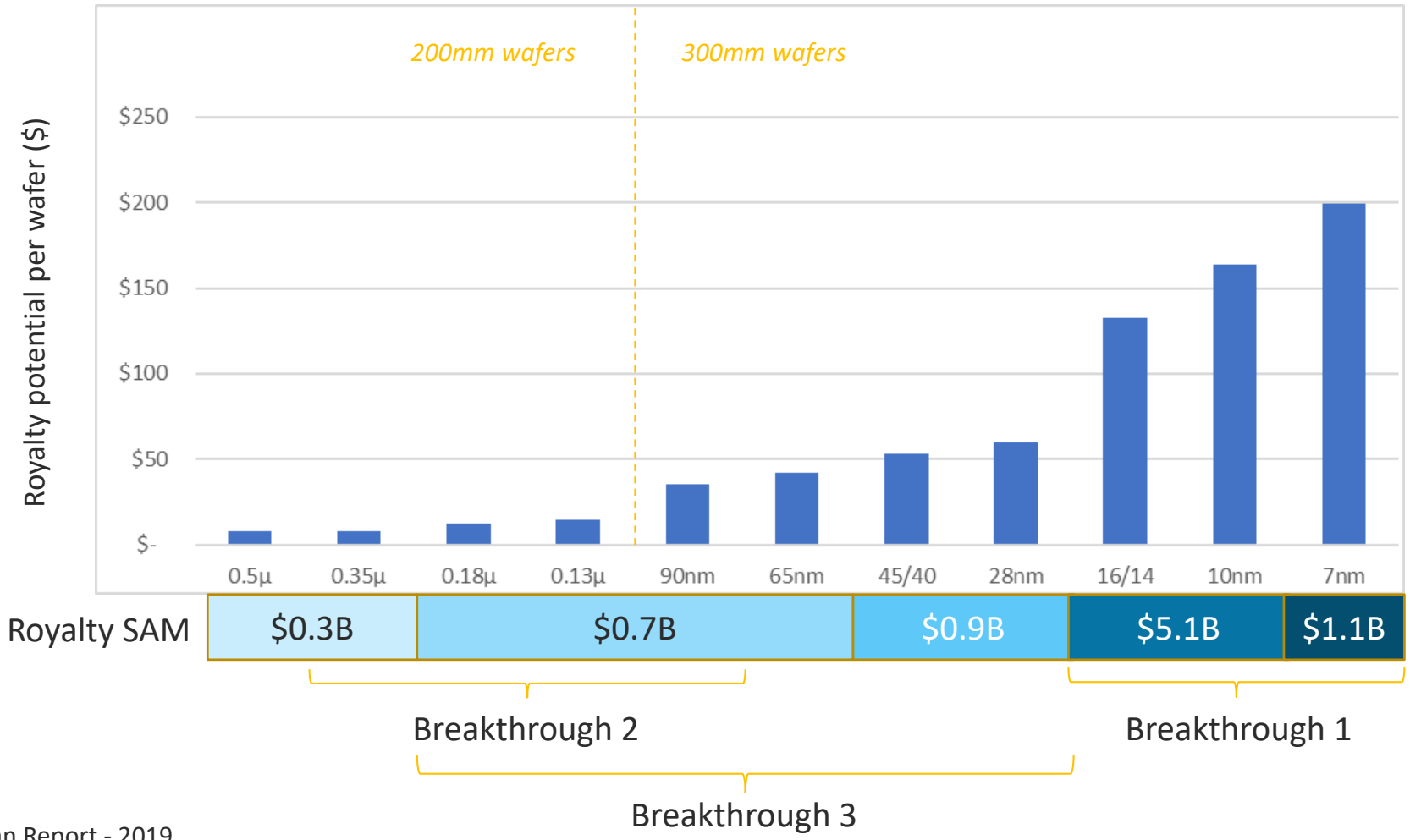
5V Analog Breakthrough

- 3. Breakthorough performance achieved on 5V analog products
- Large segment of the overall analog market
- Atomera has targeted ~20% improvement on 5V devices
- In April Atomera demonstrated a 50%+ improvement
  - Using MST-SP technology
  - Relatively fast and easy to implement
- Expected to give many business advantages
  - Time to license, accelerated time to royalty, negotiating leverage
  - Applicable to even more markets
- Market size: ~$33B, or $660M in royalties





Royalty potential per node



Source: The McClean Report - 2019

Royalty opportunity

- ~375 wafer fabs operating worldwide
- Adoption of MST in *one fab,* at 50% of capacity, can make Atomera profitable from royalties alone
  - 2019 non-GAAP OPEX guidance is $11-12M

Example 1. Worldwide Average Fab	
Monthly Fab Capacity[1] *(wafers/month)*	40,000
Industry average wafer ASP[2]	$1,637
Annual Revenue Potential[2]	***$16M***
Annual Revenue at 50% of ramp[2]	*$8M*

Example 2. Leading Foundry, 40nm Fab	
Monthly Fab Capacity[1] *(wafers/month)*	80,000
Industry average wafer ASP[2]	$3,000
Annual Revenue Potential[2]	***$58M***
Annual Revenue at 50% of ramp[2]	*$29M*

1. Represents wafers per year (200mm equ).
2. 2016 Industry wafer ASP: $1,637, 2% royalty

Source: IC Insights Global Wafer Capacity 2017-2021 report

Financial Overview

	Q1 '18	Q2 '18	Q3 '18	Q4 '18	Q1 '19
GAAP Results					
Revenue	-	$0.1M	-	$0.15M	$0.1M
Gross Profit	-	($0.02M)	-	$0.12M	$0.1M
Operating Expense	($3.1M)	($3.2M)	($3.5M)	($3.4M)	($3.7M)
Net Loss	($3.1M)	($3.2M)	($3.4M)	($3.2M)	($3.5M)
Loss Per Share	*($0.26)*	*($0.26)*	*($0.28)*	*($0.22)*	*($0.24)*
Reconciliation between GAAP & Non-GAAP					
Net Loss (GAAP)	($3.1M)	($3.2M)	($3.4M)	($3.2M)	($3.5M)
Stock-Based Compensation	$0.5M	$0.6M	$0.6M	$0.6M	($0.7M)
Adjusted EBITDA (Non-GAAP)*	($2.6M)	($2.6M)	($2.8M)	($2.6M)	($2.9M)

March 31, 2019 Liquidity	Actual	Pro Forma**
Cash	$15.1M	$21.5M
Shares Outstanding	15.3M	17.0M
Debt	$ -	$ -

**Adjusted EBITDA is a non-GAAP financial measure. A full reconciliation of GAAP and non-GAAP results is contained in our Q1 press release.*

*** Pro forma for May 29, 2019 equity financing*

Summary

- High margin, recurring revenue financial model
- Solid cash position
- Strong technology and patent position
- Traction with many top industry players and growing licensee base
- Ramping commercial license revenues

Thank You